REMARKS

This application has been carefully reviewed in light of the Office Action dated April 20, 2006. Claims 1, 5 to 9 and 13 to 17 remain in the application, of which Claims 1, 9 and 17 are independent. Reconsideration and further examination are respectfully requested.

Claims 1, 5 to 9 and 13 to 17 were rejected under 35 U.S.C. § 112, first paragraph, for allegedly including subject matter not described in the specification. Without conceding the correctness of the rejections, the claims have been amended to make the subject matter even clearer. Additionally, for the Examiner's convenience, the subject matter in question can be found at page 57, line 11 to page 62, line 9 of the specification. Reconsideration and withdrawal of the rejections are respectfully requested.

Claims 1, 5 to 9 and 13 to 17 were rejected under 35 U.S.C. § 103(a) over U.S. Patent No. 6,233,611 (Ludtke) in view of U.S. Patent No. 6,327,637 (Chang). Reconsideration and withdrawal of the rejections are respectfully requested.

The present invention relates to using a part of the predetermined addresses for indicating a bus reset status on a remote bus connected to the communication control bus via a bridge. According to the invention, when generation of a bus reset on a remote bus connected to the communication control bus via the bridge is detected by referring to the part of the predetermined addresses which is used for indicating bus reset, a high level protocol layer is informed of the generation of the bus reset. As a result, an apparatus does not execute a bus reset process on the communication control bus and the high level protocol re-establishes a connection with a node connected to the remote bus when the high

level protocol is informed of the generation of the bus reset while the high level layer establishes a connection with the node.

With specific reference to the claims, amended Claim 1 is directed to an An information signal processing apparatus connected to a connection control network, comprising event reception means for receiving a predetermined event instruction irrespective of a type of high level protocol by using predetermined addresses as registers, which are allocated in a serial bus register space in an address space of the information signal processing apparatus connected to a communication control bus complying with IEEE 1394, event generating means for generating an event corresponding to the received instruction when the event reception means receives an event instruction, detecting means for detecting generation of a bus reset on a remote bus connected to the communication control bus via a bridge by referring to a part of the predetermined addresses which is used for indicating bus reset status on the remote bus, and informing means for informing a high level protocol layer of generation of a bus reset on the remote bus when the detecting means detects the generation of the bus reset, wherein the apparatus does not execute a bus reset process on the communication control bus when the detecting means detects generation of a bus reset on the remote bus, and the high level protocol layer re-establishes a connection with a node connected to the remote bus when the informing means informs the high level protocol layer of the generation of the bus reset on the remote bus while the high level protocol layer establishes a connection with the node.

Amended independent Claims 9 and 17 are directed to a method and a program, respectively, substantially in accordance with the apparatus of Claim 1.

The applied art, alone or in any permissible combination, is not seen to disclose or to suggest the features of Claims 1, 9 and 17, and in particular, is not seen to disclose or to suggest at least the feature an apparatus informing a high level protocol layer of generation of a bus reset on a remote bus when the generation of a bus reset on the remote bus connected to a communication control bus via a bridge is detected by referring to a part of predetermined address which is used for indicating bus reset status on the remote bus, wherein the apparatus does not execute a bus reset process on the communication control bus when the generation of the bus reset on the remote bus is detected, and the high level protocol layer re-establishes a connection with a node connected to the remote bus when the high level protocol layer is informed of the generation of the bus reset on the remote bus while the high level protocol layer establishes a connection with the node.

Ludtke discloses a Device Control Module (DCM) which provides a consistent interface for a device control, including complex services such as command queuing. Ludtke requires coordination with a host operating system for device control protocol usage, including packaging, sending, processing protocol-specific commands and responses via the protocol driver and other operating system provided support mechanisms. (See Ludtke, Column 9, Lines 3 to 11). However, Ludtke is not seen to disclose or to suggest at least the feature of an apparatus informing a high level protocol layer of generation of a bus reset on a remote bus when the generation of a bus reset on the remote bus connected to a communication control bus via a bridge is detected by referring to a part of predetermined address which is used for indicating bus reset status on the remote bus, wherein the apparatus does not execute a bus reset process on the communication control

bus when the generation of the bus reset on the remote bus is detected, and the high level protocol layer re-establishes a connection with a node connected to the remote bus when the high level protocol layer is informed of the generation of the bus reset on the remote bus while the high level protocol layer establishes a connection with the node.

Chang is not seen to remedy the foregoing deficiencies of Ludtke and is not seen to disclose or to suggest anything that, when combined with Ludtke would have resulted in the features of the present invention. In this regard, Chang is merely seen to disclose the use of logic that enables multiple internal link layers to communicate with each other with the need for an intervening physical layer. However, Chang is not seen to disclose or to suggest anything that, when combined with Ludtke would have resulted in at least the feature of an apparatus informing a high level protocol layer of generation of a bus reset on a remote bus when the generation of a bus reset on the remote bus connected to a communication control bus via a bridge is detected by referring to a part of predetermined address which is used for indicating bus reset status on the remote bus, wherein the apparatus does not execute a bus reset process on the communication control bus when the generation of the bus reset on the remote bus is detected, and the high level protocol layer re-establishes a connection with a node connected to the remote bus when the high level protocol layer is informed of the generation of the bus reset on the remote bus while the high level protocol layer establishes a connection with the node.

In light of the deficiencies of Ludtke and Chang, Applicant submits that amended independent Claims 1, 9 and 17, as well as the claims dependent therefrom, are allowable.

In view of the foregoing amendments and remarks, the entire application is believed to be in condition for allowance and such action is respectfully requested at the Examiner's earliest convenience.

Applicant's undersigned attorney may be reached in our Costa Mesa, California office at (714) 540-8700. All correspondence should continue to be directed to our below-listed address.

Respectfully submitted,

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